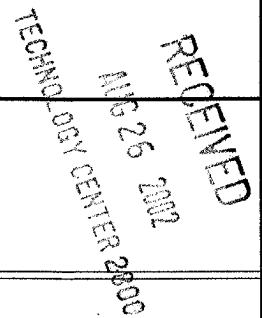
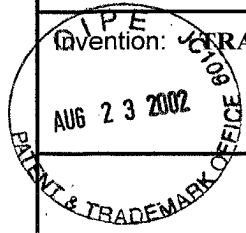


2834

<b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b> Applicant(s): <b>LIEJON</b>	Docket No. <b>66291-181</b>
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Serial No. <b>09/554,894</b>	Filing Date <b>08/25/2000</b>	Examiner <b>MULLINS</b>	Group Art Unit <b>2834</b>
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Invention: **TRACTION MOTOR AND DRIVE SYSTEM**

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	21 -	21 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	5 -	5 =	0 x	\$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. **04-2223**  
A duplicate copy of this sheet is enclosed.
  - ☒ Any additional filing fees required under 37 C.F.R. 1.16.
  - ☒ Any patent application processing fees under 37 CFR 1.17.

*Signature*

Dated: **AUGUST 23, 2002**

**JOHN P. DE LUCA  
DYKEMA GOSSETT PLLC  
1300 I STREET N.W.  
WASHINGTON, D.C. 20005-3306  
202 906 8600**

I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.
<i>Signature of Person Mailing Correspondence</i>
Typed or Printed Name of Person Mailing Correspondence

CC:

Claim 1. (Twice Amended) A 3-phase traction motor comprising a high voltage winding, wherein said winding includes insulation comprising at least two semiconducting layers, each layer providing a substantially equipotential surface, and solid insulation between said semiconducting layers.